

IPS6011(S)(R)PbF

INTELLIGENT POWER HIGH SIDE SWITCH

Features

- Over temperature shutdown (with auto-restart)
- Short circuit protection (current limit)
- Reverse battery protection (turns On the MOSFET)
- Full diagnostic capability (short circuit to battery)
- Active clamp
- Open load detection in On and Off state
- Ground loss protection
- Logic ground isolated from power ground
- ESD protection

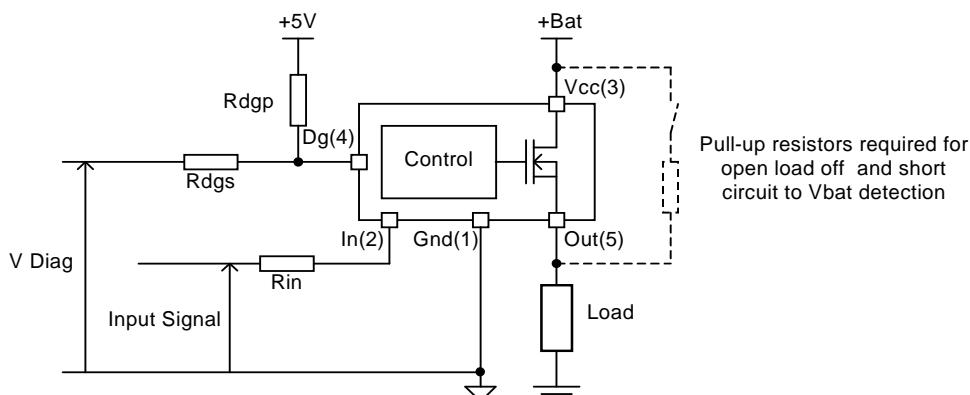
Product Summary

Rds(on)	14mΩ max.
Vclamp	39V
I Limit	60A
Open load	3V / 2.4A

Description

The IPS6011(S)(R)PbF is a five terminal Intelligent Power Switch (IPS) for use in a high side configuration. It features short circuit, over-temperature, ESD protection, inductive load capability and diagnostic feedback. The output current is limited to the Ilim value. The current limitation is activated until the thermal protection acts. The over-temperature protection turns off the device if the junction temperature exceeds the Tshutdown value. It will automatically restart after the junction has cooled 7°C below the Tshutdown value. The reverse battery protection turns On the MOSFET. A diagnostic pin provides different voltage levels for each fault condition. The double level shifter circuitry will allow large offsets between the logic and load ground.

Typical Connection



Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are referenced to Ground lead. (T_{ambient}=25°C unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
V _{out}	Maximum output voltage	V _{cc} -35	V _{cc} +0.3	V
V _{offset}	Maximum logic ground to load ground offset	V _{cc} -35	V _{cc} +0.3	
V _{in}	Maximum input voltage	-0.3	5.5	
V _{cc} max.	Maximum V _{cc} voltage	—	36	
V _{cc} cont.	Maximum continuous V _{cc} voltage	—	28	
V _{cc} sc.	Maximum V _{cc} voltage with short circuit protection	—	24	
I _{in} max.	Maximum IN current	-3	10	
I _{dg} max.	Maximum diagnostic output current	-3	10	mA
V _{dg}	Maximum diagnostic output voltage	-0.3	5.5	V
P _d	Maximum power dissipation (internally limited by thermal protection)	—	—	W
	R _{th} =5°C/W IPS6011	—	25	
	R _{th} =40°C/W IPS6011S 1"sqrt. footprint	—	3.1	
	R _{th} =50°C/W IPS6011R 1"sqrt. footprint	—	2.5	
ESD	Electrostatic discharge voltage (Human body) C=100pF, R=1500Ω	—	1500	V
	Between In and V _{cc}	—	4000	
	Other combinations	—	—	
	Electrostatic discharge voltage (Machine Model) C=200pF, R=0Ω, L=10µH	—	100	
	Between In and V _{cc}	—	500	
T _j max.	Max. storage & operating temperature junction temperature	-40	150	°C
T _{soldering}	Soldering temperature (10 seconds)	—	300	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
R _{th1}	Thermal resistance junction to ambient IPS6011 TO220 free air	50	—	°C/W
R _{th2}	Thermal resistance junction to case IPS6011 TO220	1.2	—	
R _{th1}	Thermal resistance junction to ambient IPS6011S D ² Pak std. footprint	60	—	
R _{th2}	Thermal resistance junction to ambient IPS6011S D ² Pak 1" sqrt. footprint	40	—	
R _{th3}	Thermal resistance junction to case IPS6011S D ² Pak	1.2	—	
R _{th1}	Thermal resistance junction to ambient IPS6011R D-Pak std. footprint	70	—	
R _{th2}	Thermal resistance junction to ambient IPS6011R D-Pak 1" sqrt. footprint	50	—	
R _{th3}	Thermal resistance junction to case IPS6011R D-Pak	1.2	—	

Recommended Operating Conditions

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
V _{IH}	High level input voltage	4	5.5	
V _{IL}	Low level input voltage	0	0.9	
I _{out}	Continuous drain current, T _{ambient} =85°C, T _j =125°C, V _{in} =5V	—	18	A
	R _{th} =5°C/W IPS6011	—	6.3	
	R _{th} =40°C/W IPS6011S 1" sqrt. footprint	—	5.6	
	R _{th} =50°C/W IPS6011R 1" sqrt. footprint	—	—	
R _{in}	Recommended resistor in series with IN pin	4	10	kΩ
R _{dg}	Recommended resistor in series with DG pin for reverse battery protection	4	20	
R _{dg}	Recommended pull-up resistor for DG	4	20	
R _{ol}	Recommended pull-up resistor for open load detection	5	100	
F max.	Max. switching frequency	—	0.3	kHz

Static Electrical Characteristics

T_j=25°C, V_{cc}=14V (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
R _{ds(on)}	ON state resistance T _j =25°C	—	11	14	mΩ	V _{in} =5V, I _{out} =20A
	ON state resistance T _j =150°C	—	19.5	25		V _{in} =5V, I _{out} =20A
	ON state resistance T _j =25°C, V _{cc} =6V	—	12	17		V _{in} =5V, I _{out} =20A
	ON state resistance during reverse battery	—	15	20		V _{cc} -Gnd=14V
V _{cc} op.	Operating voltage range with short circuit protection	6	—	24	V	
V clamp 1	V _{cc} to Out clamp voltage 1	37	39	—		I _{out} =50mA
V clamp 2	V _{cc} to Out clamp voltage 2	—	40	42		I _{out} =16A (see Fig. 1)
I _{cc} Off	Supply current when Off	—	4	9	μA	V _{in} =0V, V _{out} =0V
I _{cc} On	Supply current when On	—	2.2	5	mA	V _{in} =5V
V _{ih}	Input high threshold voltage	—	2.5	2.9	V	
V _{il}	Input low threshold voltage	1.5	2	—		
In hyst.	Input hysteresis	0.2	0.5	1		
I _{in} On	Input current when device is On	—	40	100	μA	V _{in} =5V
I _{dg}	Dg leakage current	—	0.1	10		V _{dg} =5V
V _{dg}	Low level DG voltage	—	0.25	0.4	V	I _{dg} =1.6mA

Switching Electrical Characteristics

V_{cc}=14V, Resistive load=1Ω, V_{in}=5V, T_j=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
T _d on	Turn-on delay time	—	30	60	μs	see Fig. 3
T _r 1	Rise time to V _{out} =V _{cc} -5V	—	25	50		
T _r 2	Rise time to V _{out} =0.9 x V _{cc}	—	40	100		
dV/dt (On)	Turn On dV/dt	—	0.3	0.8		
E _{On}	Turn On energy	—	4	—		
T _d off	Turn-off delay time	—	70	140		
T _f	Fall time to V _{out} =0.1 x V _{cc}	—	30	80		
dV/dt (Off)	Turn Off dV/dt	—	0.7	2		
E _{Off}	Turn Off energy	—	1.5	—		

Protection Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Ilim	Internal current limit	35	60	85	A	Vout=0V
Tsd+	Over temperature high threshold	150(1)	165	—	°C	See fig. 2
Tsd-	Over temperature low threshold	—	158	—		
Vsc	Short-circuit detection voltage(2)	2	3	4		
UV+	High undervoltage lockout	—	5	5.9	V	
UV-	Low undervoltage lockout	3.2	4.4	5.8		
VOL Off	Open load detection threshold	2	3	4		
IOL On	Open load detection threshold	0.5	1.6	2.4	A	

(1) Guaranteed by design

(2) Reference to Vcc

True Table

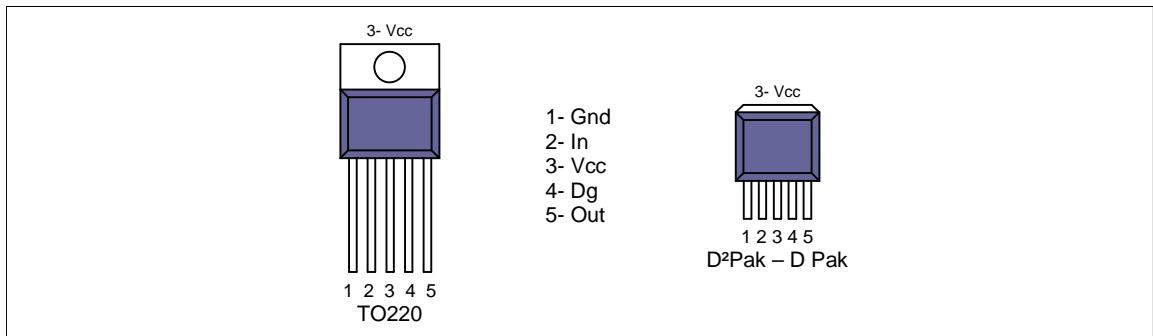
Operating Conditions	IN	OUT	DG
Normal	H	H	H
Normal	L	L	H
Open Load	H	H	L
Open Load (3)	L	H	L
Short circuit to Gnd	H	L	L
Short circuit to Gnd	L	L	H
Short circuit to Vcc	H	H	L (4)
Short circuit to Vcc (5)	L	H	L
Over-temperature	H	L	L
Over-temperature	L	L	H

(3) With a pull-up resistor connected between the output and Vcc.

(4) Vds lower than 10mV.

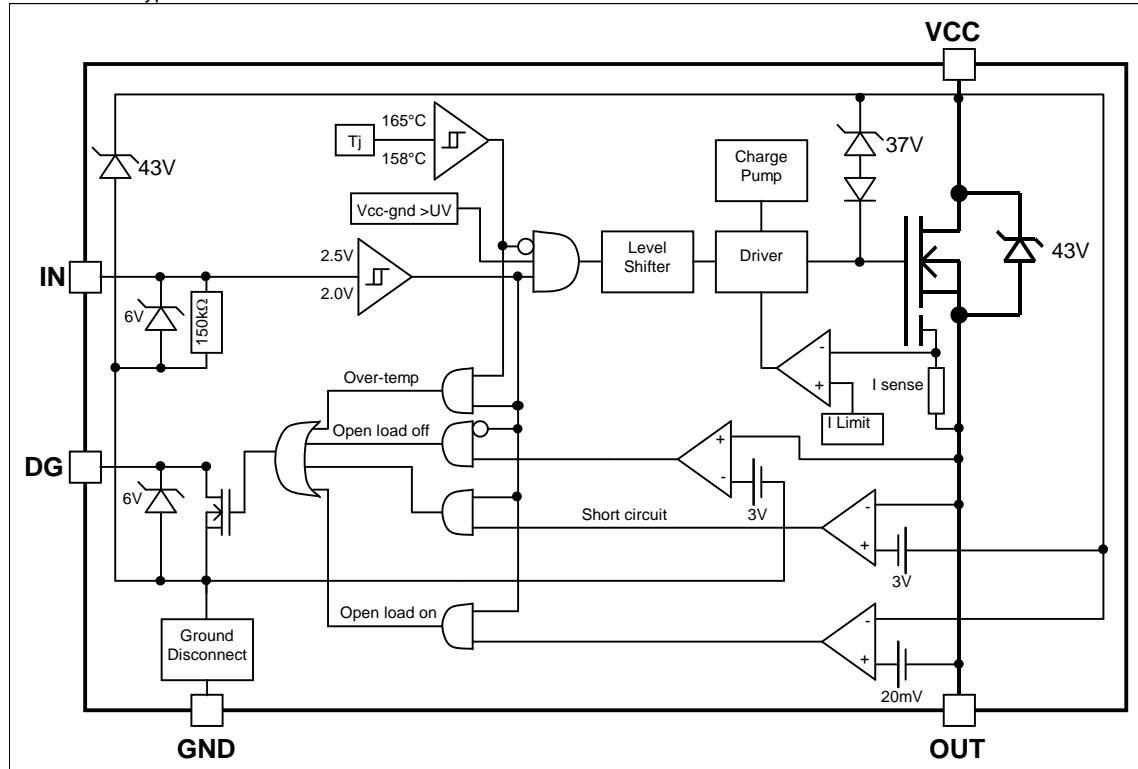
(5) Without a pull-up resistor connected between the output and Vcc.

Lead Assignments



Functional Block Diagram

All values are typical



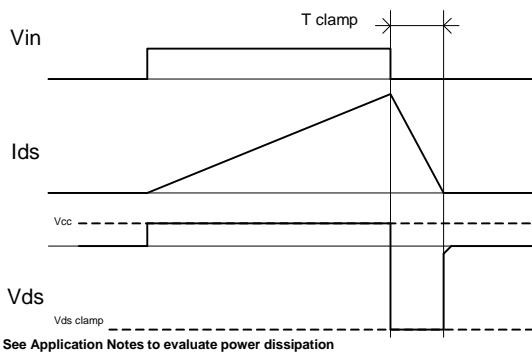


Figure 1 – Active clamp waveforms

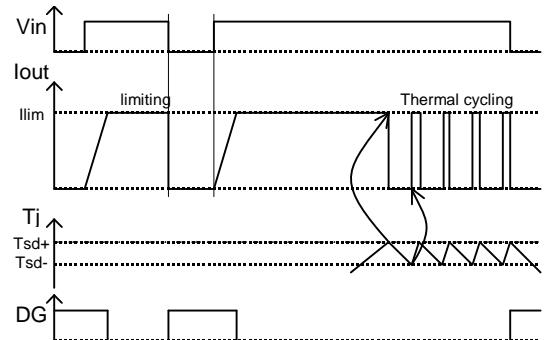


Figure 2 – Protection timing diagram

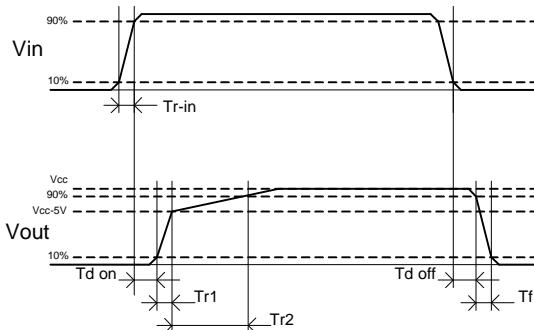


Figure 3 – Switching times definitions

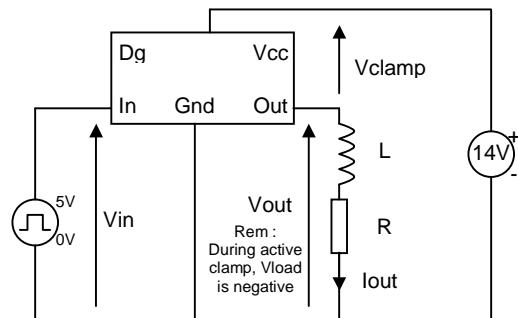


Figure 4 – Active clamp test circuit

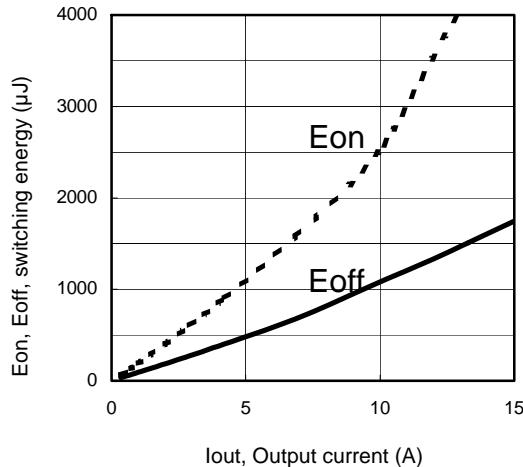


Figure 5 – Switching energy (μJ) Vs Output current (A)

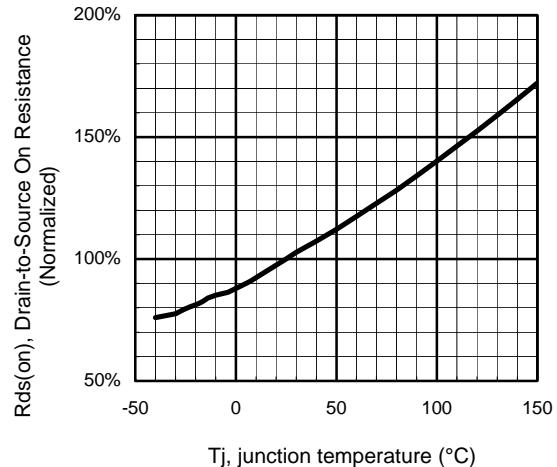


Figure 6 - Normalized R_{d(on)} (%) Vs T_j (°C)

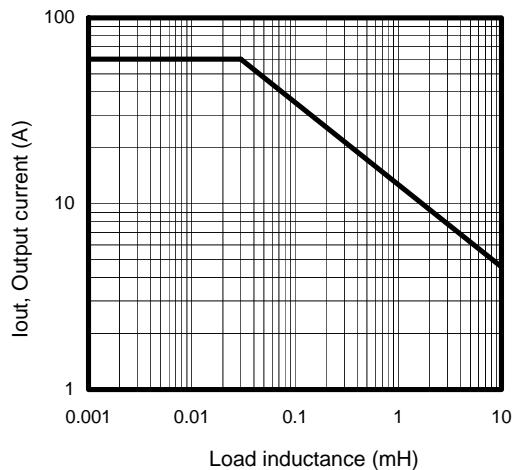


Figure 7 – Max. Output current (A) Vs Load inductance (mH)

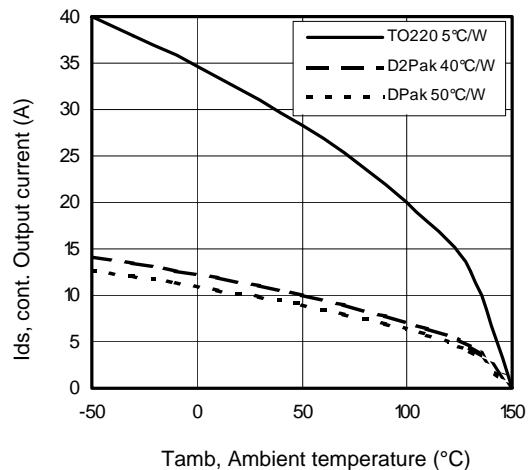
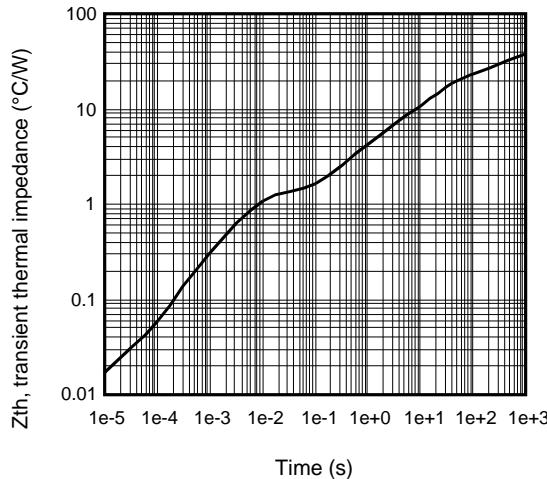
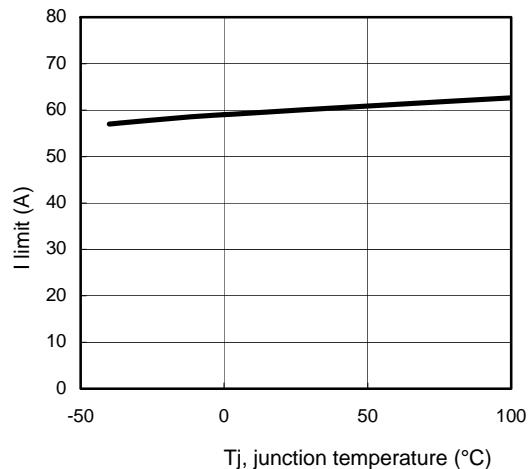


Figure 8 – Max. output current (A) Vs Ambient temperature (°C)



**Figure 9 – Transient thermal impedance ($^{\circ}\text{C/W}$)
Vs time (s)**



**Figure 10 – I_{limit} (A)
Vs junction temperature ($^{\circ}\text{C}$)**

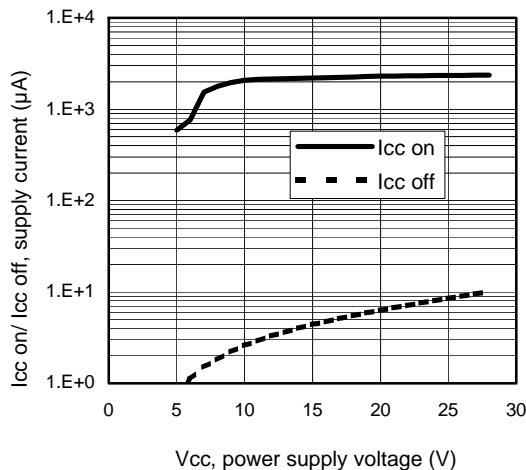


Figure 11 – $I_{cc\ on}/I_{cc\ off}$ (μA) Vs V_{cc} (V)

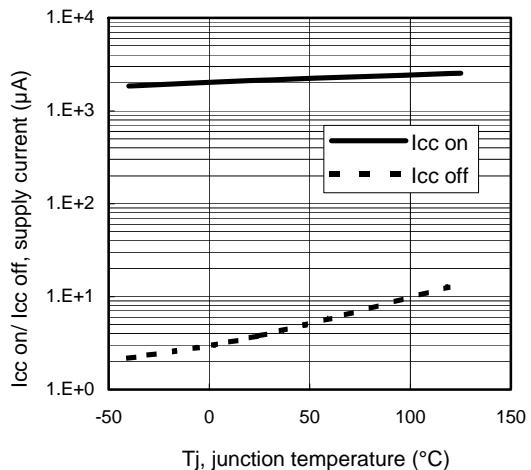
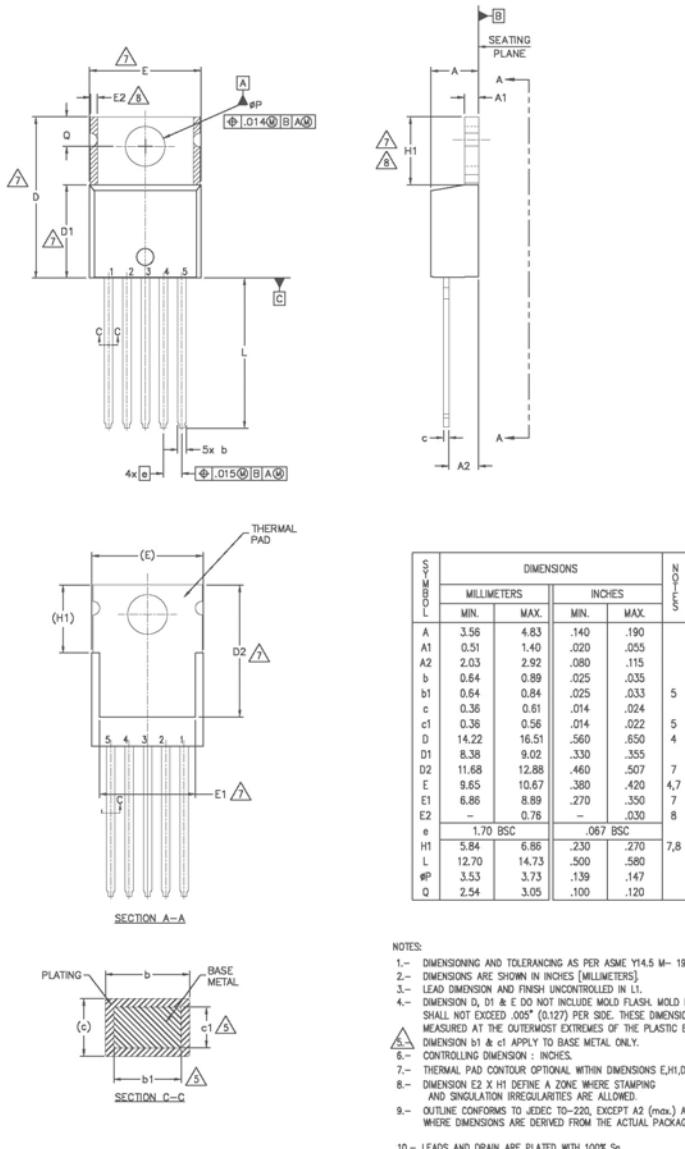
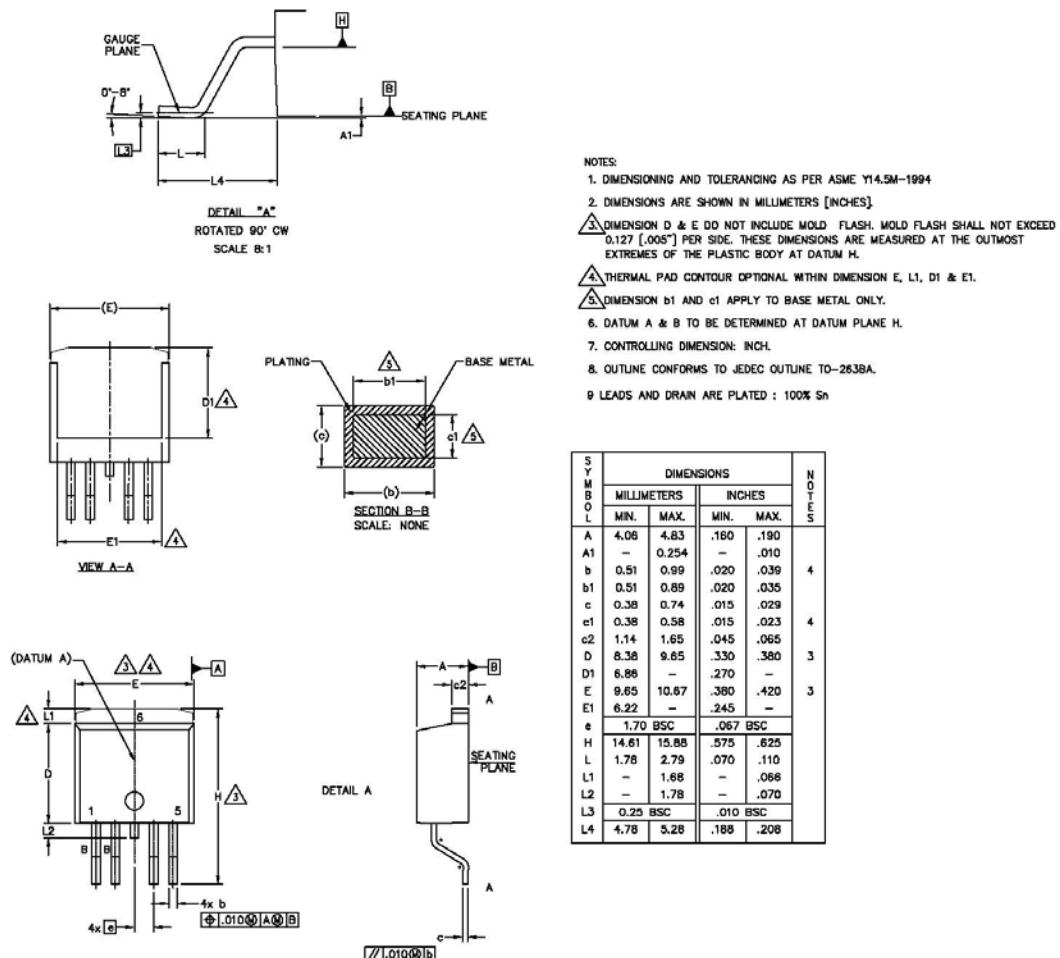


Figure 12 – $I_{cc\ on}/I_{cc\ off}$ (μA) Vs T_j ($^{\circ}\text{C}$)

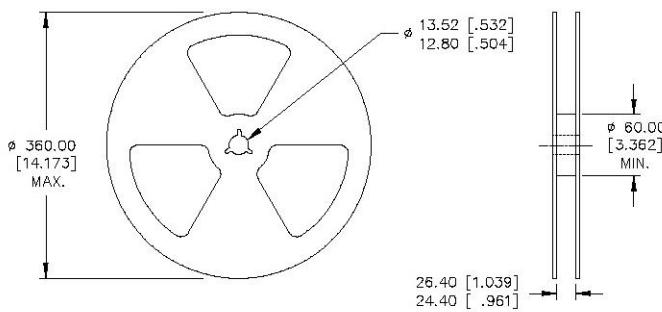
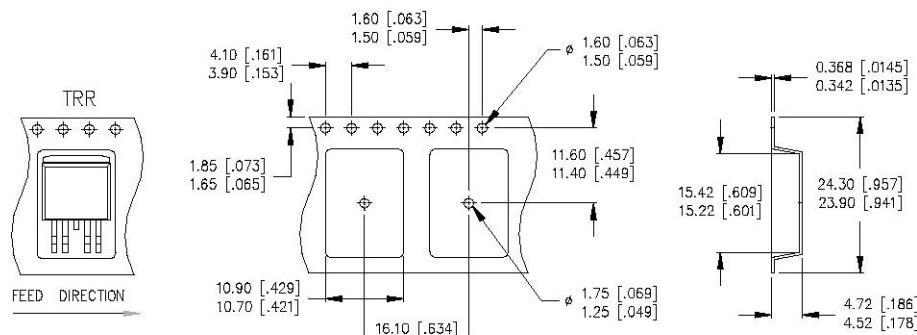
Case Outline - TO220 (5 lead) - Automotive Q100 PbF qualified



Case Outline 5 Lead - D2PAK - Automotive Q100 PbF MSL1 qualified



Tape & Reel 5 Lead - D2PAK

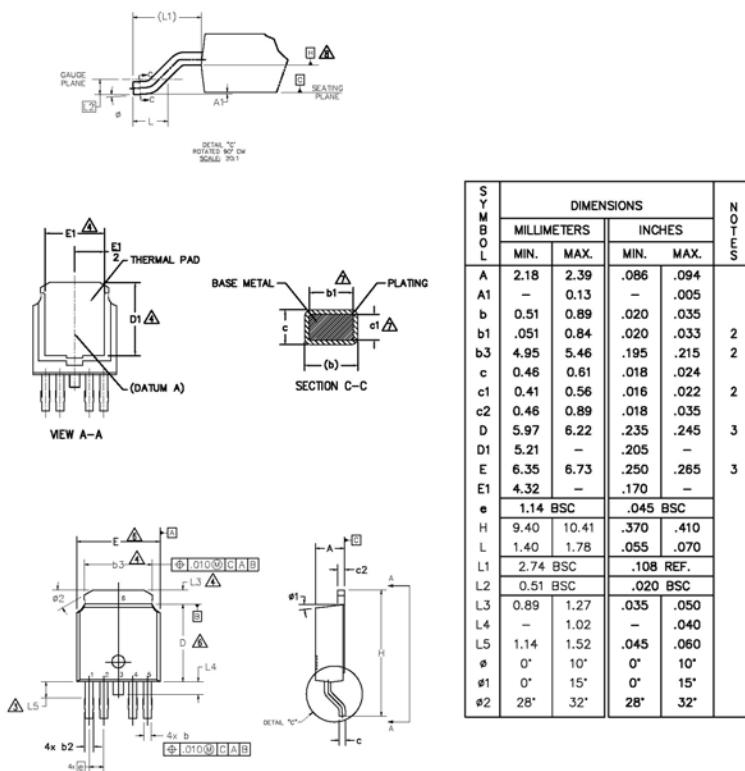


NOTES:

1. OUTLINE CONFORMS TO EIA-481 & EIA-541.
2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].

01-3071 00 / 01-3072 00

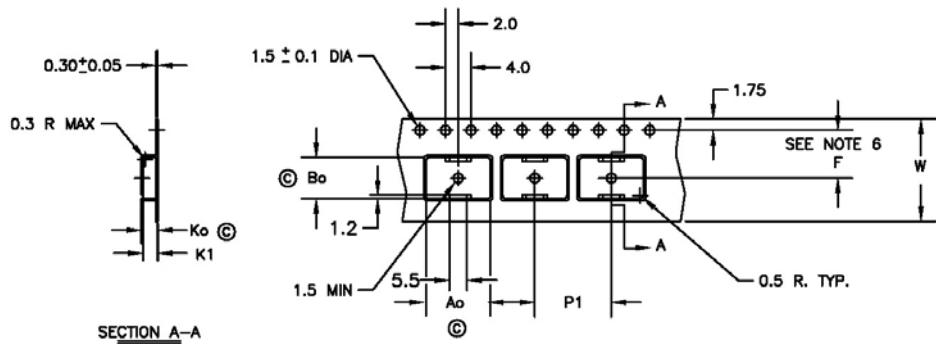
Case Outline 5 Lead – DPAK - Automotive Q100 PbF MSL1 qualified



NOTES:

- 1.- DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M-1994
- 2.- DIMENSION ARE SHOWN IN INCHES [MILLIMETERS]
- 3.- LEAD DIMENSION UNCONTROLLED IN L5.
- 4.- DIMENSION D1, E1, L3 & b3 ESTABLISH A MINIMUM MOUNTING SURFACE FOR THERMAL PAD.
- 5.- SECTION C-C DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN .005 AND 0.10 [0.13 AND 0.25] FROM THE LEAD TIP.
- 6.- DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005 [0.13] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
- 7.- DIMENSION b1 & c1 APPLIED TO BASE METAL ONLY.
- 8.- DATUM A & B TO BE DETERMINED AT DATUM PLANE H.
- 9.- OUTLINE CONFORMS TO JEDEC OUTLINE TO-252.
10. LEADS AND DRAIN ARE PLATED WITH 100% Sn

Tape & Reel 5 Lead – DPAK



Ao = 10.5 mm
Bo = 7.0 mm
Ko = 2.8 mm
K1 = 2.4 mm
F = 7.5 mm
P1 = 12.0 mm
W = 16.0 ± .3 mm

- NOTES:

 1. 10 SPROCKET HOLE PUNCH CUMULATIVE TOLERANCE $\pm .02$
 2. CAMBER NOT TO EXCEED 1mm IN 100mm
 3. MATERIAL: CONDUCTIVE BLACK POLYSTYRENE
 4. A_0 AND B_0 MEASURED ON A PLANE 0.3mm ABOVE THE BOTTOM OF THE POCKET
 5. K_0 MEASURED FROM A PLANE ON THE INSIDE BOTTOM OF THE POCKET TO THE TOP SURFACE OF THE CARRIER
 6. POCKET POSITION RELATIVE TO THE SPROCKET HOLE MEASURED AS TRUE POSITION OF POCKET, NOT POCKET HOLE
 7. VENDOR: (OPTIONAL)
 8. MUST ALSO MEET REQUIREMENTS OF EIA STANDARD #EIA-481A, TAPING OF SURFACE-MOUNT COMPONENTS FOR AUTOMATIC PLACEMENT.
 9. TOLERANCE TO BE MANUFACTURER STANDARD
 10. SURFACE RESISTIVITY OF MOLDED MATL: MUST MEASURE LESS THAN OR EQUAL TO 10^8 OHMS PER SQUARE. MEASURED IN ACCORDANCE TO PROCEDURE GIVEN IN ASTM D-257 & ASTM D-991 (REF. C-9000 SPEC.)
 11. TOTAL LENGTH PER REEL MUST BE 79 METERS
 12. © CRITICAL DIMENSION

IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245 Tel: (310) 252-7105
Data and specifications subject to change without notice

This product has been designed and qualified for the Automotive [Q1001] market. 29/10/2010

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